

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S86	15122	((703/13-22) or (716/1-21)).CCLS.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/05/11 10:13
S87	53	S86 and (simulat\$4 with circuit) and (partition\$4 with circuit) and (group\$3 with circuit\$2)	US-PGPU B	OR	ON	2007/05/11 10:24
S88	53	S86 and (simulat\$4 with circuit) and (partition\$4 with circuit) and (group\$3 with circuit\$2)	US-PGPU B	OR	ON	2007/05/11 10:12
S89	3	S88 and (stor\$4 with group with circuit)	US-PGPU B	OR	ON	2007/05/11 10:14
S90	1	S87 and (distribut\$4 with group with circuit)	US-PGPU B	OR	ON	2007/05/11 10:16
S91	2214	(simulat\$4 with circuit) and (hierarchic\$3 with structure\$2)	US-PGPU B	OR	ON	2007/05/11 10:19
S92	2	S91 and (isomorph\$4 with event)	US-PGPU B	OR	ON	2007/05/11 10:20
S93	15	S91 and (adaptive with event)	US-PGPU B	OR	ON	2007/05/11 10:20
S94	373	(simulat\$4 with circuit) and (hierarchic\$3 with structure\$2)	US-PGPU B	OR	ON	2007/05/11 10:19
S95	2	S94 and (isomorph\$4 with event)	US-PGPU B	OR	ON	2007/05/11 10:20
S96	4	(global with accept\$4 with time) and (circuit with simulat\$4)	US-PGPU B	OR	ON	2007/05/11 10:22

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S97	6	(local with accept\$4 with time) and (circuit with simulat\$4)	US-PGPU B	OR	ON	2007/05/11 10:22
S98	114	(simulat\$4 with circuit) and (partition\$4 with circuit) and (group\$3 with circuit\$2)	US-PGPU B	OR	ON	2007/05/11 10:25
S99	47	S98 and hierarchi\$3	US-PGPU B	OR	ON	2007/05/11 10:25
S100	7	S99 and isomorph\$4	US-PGPU B	OR	ON	2007/05/11 10:25
S101	2	("6807520").PN.	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	OFF	2007/07/18 13:52
S102	84	(bdd and (dynamic with (memory or storage)))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/18 13:53
S103	43	S102 and simulat\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/18 13:55

## EAST Search History

S10 6	2	S102 and (port with connectivity and interface)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/18 13:54
S10 7	2	S103 and (port with connectivity and interface)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/18 13:54
S11 1	10	S102 and (hierarchical\$3 and data)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/21 16:51
S11 2	84	(bdd and (dynamic with (memory or storage)))	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/21 16:51

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S11 3	10	S112 and (hierarchical\$3 and data)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/21 16:51
S11 4	1	S113 and (circuit with redu\$5)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/21 16:52
S11 5	48	(port with connectivity with interface) and simulat\$4	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/21 17:41
S11 6	2	S115 and (leaf with circuit)	US-PGPU B; USPAT; USOCR; EPO; JPO; DERWEN T; IBM_TDB	OR	ON	2007/07/21 17:42